

## DIODE PROPERTY OF n-ZnO/p-Si HETEROJUNCTION STRUCTURE IN THE DARK AND ILLUMINATION CONDITION

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### Abstract

In this study, Zinc Oxide (ZnO) thin film was deposited on the Silicon (Si) wafer by Pulsed Laser Deposition (PLD) to form n-ZnO/p-Si heterojunction. The morphological and the crystal structure of ZnO thin film was analysed and interpreted by Atomic Force Microscopy (AFM) and X-Ray Diffraction (XRD), respectively. The absorption spectrum was obtained by using the UV-Vis spectra and the band gap ( $E_g$ ) was found by using Tauc Law. The current density-Voltage (J-V) plot was obtained at room temperature (RT) in the dark and under illumination. The barrier height (BH) and ideality factor were found about 0.46 eV and 1.35, respectively. The largest values of open circuit voltage ( $V_{oc}$ ) and short-circuit current ( $J_{sc}$ ) were about 100 mV and  $3 \times 10^{-2}$  mA/cm<sup>2</sup>, respectively. It has been measured that ZnO/Si heterojunction diode behaves a solar cell like device under the illumination conditions.

**Keywords:** Heterojunction, open circuit voltage, short circuit current, barrier height, ideality factor

## KARANLIK ve AYDINLIK ŞARTLARDA n-ZnO/p-Si HETEROEKLEM YAPISININ DİYOT ÖZELLİĞİ

### Özet

Bu çalışmada, n-ZnO/p-Si heteroeklem oluşturmak için Silikon (Si) wafer üzerine Çinko Oksit (ZnO) ince film Puls Lazer Deposizasyon (PLD) ile depozit edilmiştir. ZnO ince filmin morfolojik ve kristal yapısı, sırasıyla Atomik Kuvvet Mikroskobu (AFM) ve

X-Ray Kırınımı (XRD) ile analiz edilmiş ve yorumlanmıştır. Soğurma spektrumu UV-Vis spektrumu kullanılarak elde edilmiş ve bant aralığı ( $E_g$ ) Tauc yasası kullanılarak bulunmuştur. Akım yoğunluğu-Gerilim (J-V) grafiği, oda sıcaklığında (RT) karanlıkta ortamda ve ışık altında elde edilmiştir. Bariyer yüksekliği (BH) ve idealite faktörü sırasıyla yaklaşık 0.46 eV ve 1.35 bulunmuştur. Açık devre voltajının ( $V_{oc}$ ) ve kısa devre akım yoğunluğunun ( $J_{sc}$ ) en büyük değerleri sırasıyla 100 mV ve  $3 \times 10^{-2}$  mA/cm<sup>2</sup>'dir. ZnO/Si heteroeklem diyotunun ışık altında güneş piline benzer bir cihaz gibi davrandığı gözlemlenmiştir.

**Anahtar Kelimeler:** Heteroeklem, açık devre voltajı, kısa devre akımı, bariyer yüksekliği, idealite faktörü

## 1. Introduction

Transparent Conductive Oxide (TCO) semiconductor thin films have presented some excellent properties such as simple processing steps, excellent blue response and low processing temperature to form p-n heterojunction diodes with a single crystalline silicon wafer structures [1]. ZnO semiconductor material has been used as a TCO material which has some important and unique properties such as wide direct band gap (3.37 eV), large exciton binding energy (60 meV), non-toxicity [2], good electrical conductivity, transparency, low cost of production, high thermal and mechanical stabilities and find a wide application area in electronics and optoelectronics. ZnO thin film exhibiting significant n-type conductivity without any doping process is transparent in the visible region of the spectrum [3], used in sensor technology, photodetectors, multiple quantum well structures [4], light emitting diodes, p-n heterojunction diodes [5, 6], solar cells, piezoelectric devices [7], flat-panel displays [8] and thin film transistors [9]. The single crystal Si material, which is used in n-ZnO/p-Si heterojunction structure has a natural abundance and easy to process, is one of an ideal quality substrate for high-quality ZnO thin film growth [10, 11].

Several methods, such as molecular beam epitaxy (MBE), magnetron sputtering, sol-gel process, metal organic chemical vapor deposition (MOCVD) and pulsed laser deposition (PLD), are used in the production of ZnO thin film [12]. PLD is a powerful and versatile technique having several advantageous for the easy production of ZnO thin film at relatively low cost production process and low process temperature. The thin films

can be produced in crystal, epitaxial and stoichiometric structure at low substrate temperature by PLD technique [13-20]. Therefore, PLD technique has many distinguishing features to produce high-quality ZnO thin film on Si wafer substrate [2]. In this study, we have produced a n-ZnO/p-Si heterojunction diode by PLD technique. The morphology and crystal structure of ZnO thin films were investigated by Atomic Force Microscopy (AFM) and X-Ray Diffraction (XRD), respectively. Its absorption spectrum was obtained by using a UV-vis spectrometer. The current density-voltage (J-V) plot was measured at room temperature (RT) in the dark and under illumination conditions. The solar cell behaviour of n-ZnO/p-Si heterojunction diode was investigated and interpreted in the following sections.

## 2. Experimental

ZnO thin film as an n-type layer and Ag thin film back contact in Ag/ZnO/Si/Ag heterojunction structure were grown by PLD technique. Pulsed laser beam was used at 1064 nm wavelength with 40 mJ laser energy per pulse and 5 ns width at a 10 Hz repetition rate. The biconvex lens having a focal length of 50 cm was used to focus the laser beam onto the target material. The distance between the target and substrate was set to 50 mm. Before the experiment started, the vacuum chamber was evacuated down to  $1 \times 10^{-6}$  mbar. The p-type Si wafer (at (110) crystal orientation) was used as an active layer in 500  $\mu\text{m}$  thickness and 1.44  $\text{cm}^2$  area.

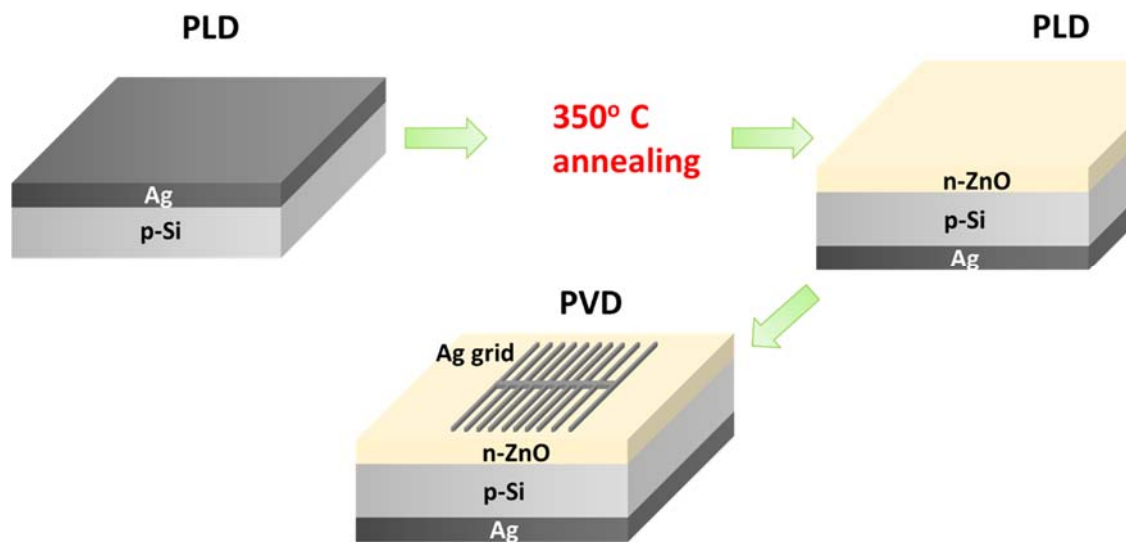


Figure 1. The production steps of Ag/n-ZnO/p-Si/Ag heterojunction diode.

Ag thin film was deposited on the back side of Si (110) wafer at RT for two hours, it was annealed at 350 °C for 5 minutes and then the ohmic contact was formed. Later, O<sub>2</sub> gas was let in to the vacuum chamber up to a pressure of  $1.3 \times 10^{-1}$  mbar. ZnO thin film was grown on the front side of Si wafer at RT for 95 minutes. The mask was placed on the thin film produced and the Ag thin film fingers were grown on ZnO thin film at RT as a front contact by applying Physical Vapor Deposition (PVD) technique (Figure 1).

### 3. Results and Discussion

#### 3.1. Structure and optical properties of ZnO thin film

According to AFM image in Figure 2a, ZnO thin film consists of small particles has non-homogeneous distribution and density. The roughness value is about 19 nm (Figure 2b.) and, therefore, it does not show high porosity and it is close to the compact structure.

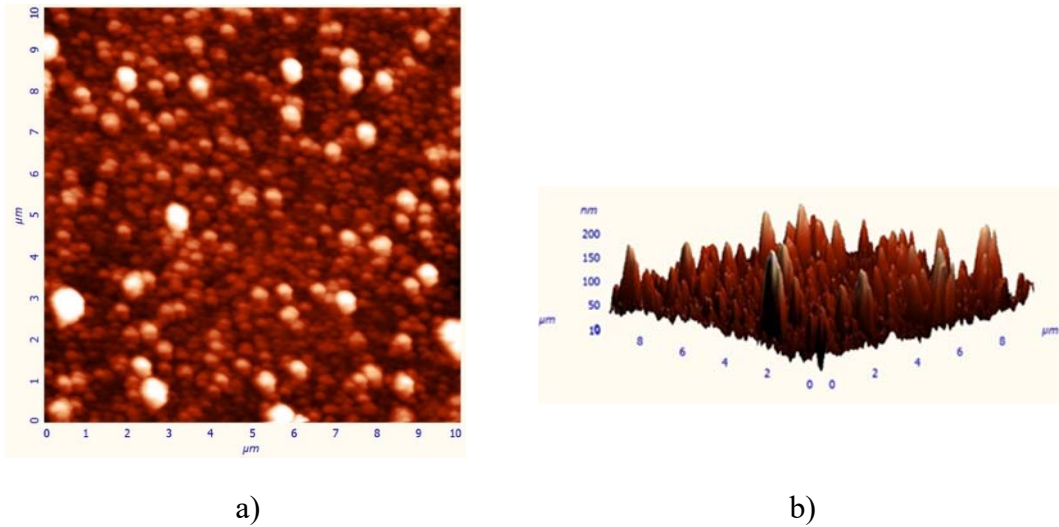


Figure 2. a) AFM (10μm×10μm area) and b) roughness images of ZnO thin film

The XRD spectrum of ZnO thin film is given in Figure 3. ZnO thin film grown at RT which has a main peak at (002) orientation and peaks at (100), (101) orientations. The fact that the ZnO thin film is in polycrystalline structure indicates that there are defects in the crystal structure, but the predominance of (002) peak shows that thin film is close to the quality ZnO structure [2, 7]. In addition, ZnO thin film growth in crystal structure at room temperature by the PLD system that affects the performance of the diode structure positively.

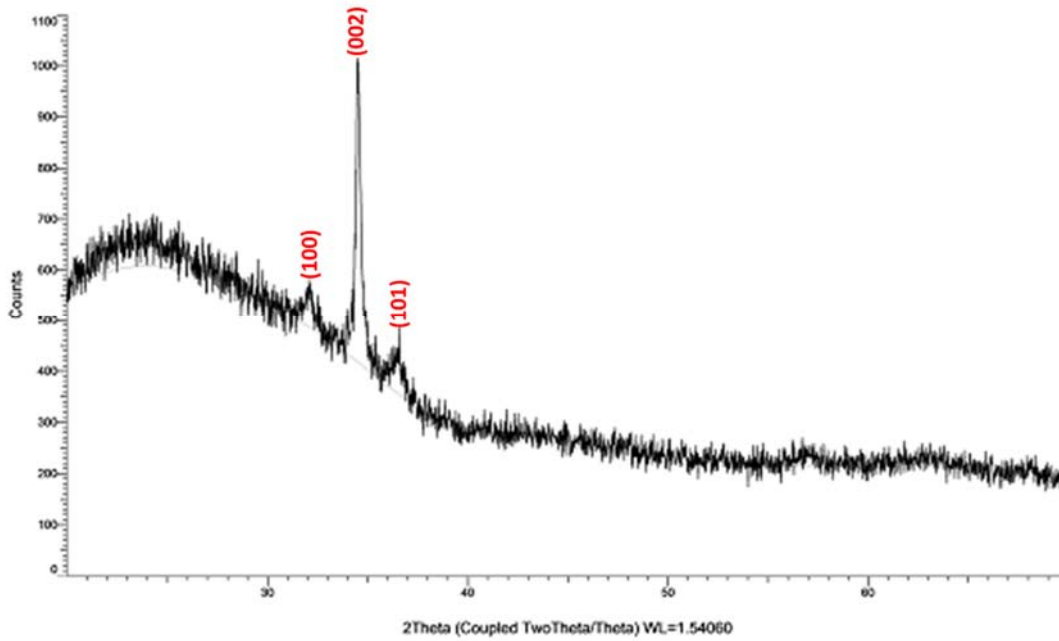


Figure 3. The XRD spectrum of ZnO thin film

According to the absorption spectrum in Figure 4, the ZnO thin film is transparent to the visible region between 500 and 700 nm, while the IR region is absorbed. The high electron carrier concentration causes the absorption of electromagnetic radiation in visible region between 450-500 nm and the infrared portions of the spectrum (materials are considered to be n-type) [21].

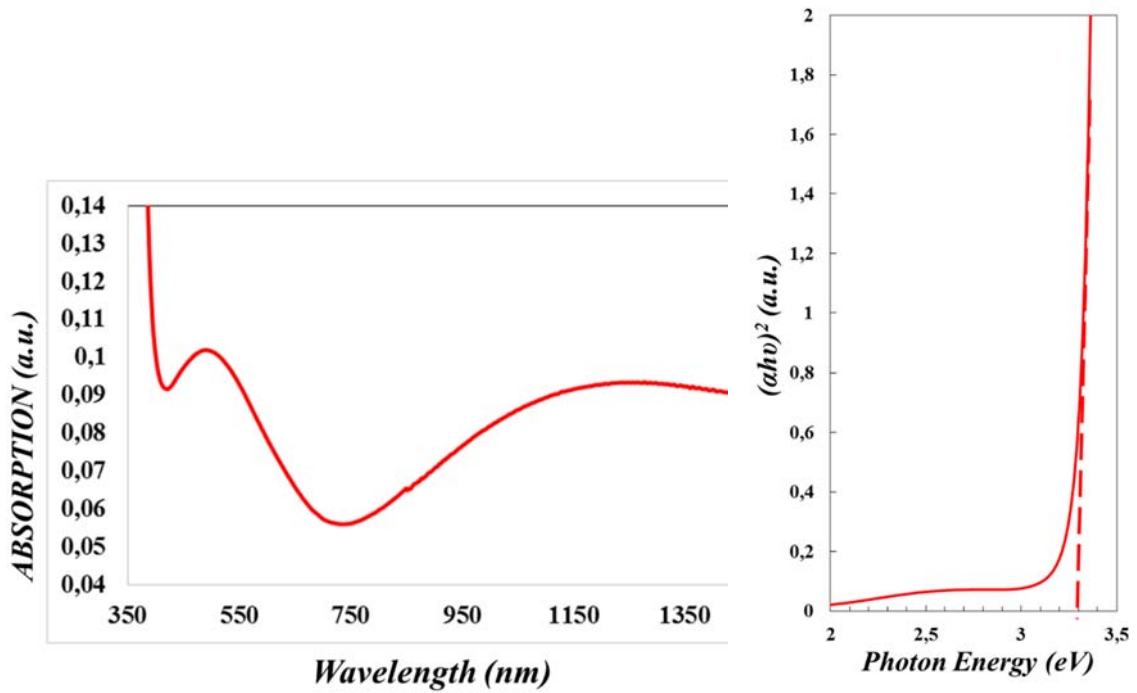
The band gap energy ( $E_g$ ) of ZnO thin film was defined by Tauc law in equation (1):

$$(\alpha hv)^2 = A(hv - E_g) \quad (1)$$

where,  $A$  is constant,  $hv$  is the photon energy,  $E_g$  is the band gap value. The linear portion of  $(\alpha hv)^2$  versus  $hv$  in the plot that gives  $E_g$  as seen Figure 4b.  $E_g$  of ZnO thin film was determined about 3,3 eV which is in good agreement with literature works [2, 22] and at this point, ZnO thin film has transparent conductive oxide (TCO) semiconductor optical properties.

### 3.2. Current density-voltage characteristics of Ag/n-ZnO/p-Si/Ag

Figure 5 shows the current density-voltage (J-V) characteristics of Ag/n-ZnO/p-Si/Ag structure presented in the dark and under the illumination at RT. It is shown that the semi-logarithmic forward bias J-V characteristics under the illumination in the inset in Figure 5.



a) b)  
Figure 4. a) The absorption spectrum and b) Tauc plot of ZnO thin film.

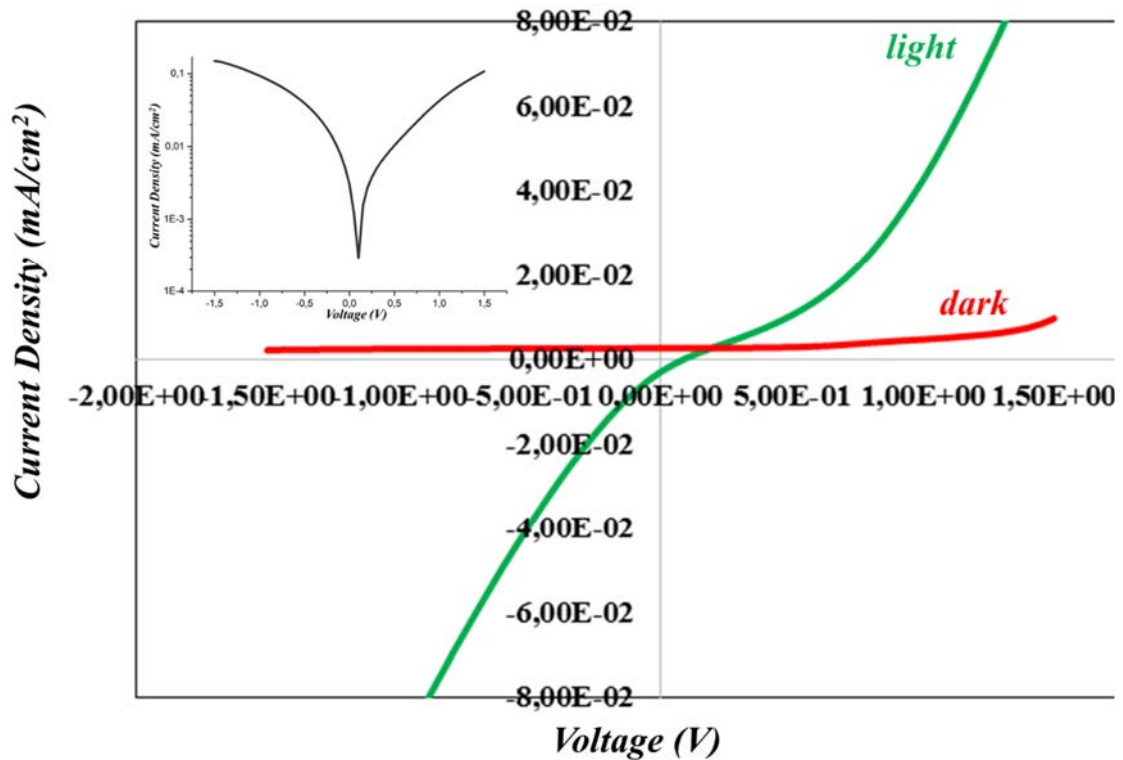


Figure 5. J-V graphic of n-ZnO/p-Si heterojunction in the dark and under the illumination

According to J-V plot, it is observed that n-ZnO/p-Si diode show some rectifying behaviour but not an ideal rectifying behaviour [23]. This behaviour of ZnO/Si heterojunction can be analysed by the expression [24] in equation (2):

$$J = J_o \left[ \left( \exp \left( \frac{qV}{nkT} \right) \right) - 1 \right] \quad (2)$$

where  $J_o$  is the saturation current density,  $q$  is the electronic charge,  $V$  is the applied voltage,  $n$  is ideality factor,  $k$  is Boltzman's constant and  $T$  is the temperature in K. The saturation current density ( $J_o$ ) can be written by following expression [25],

$$J_o = A^* T^2 e^{q\phi_b/kT} \quad (3)$$

where  $A^*$  is theoretical Richardson constant and  $32 \text{ A/cm}^2\text{K}^2$  for ZnO,  $\phi_b$  is barrier height. The value of  $J_o$  is determined to be  $2.5 \times 10^{-2} \text{ mA/cm}^2$  from straight line of  $\ln J$  curve to  $V = 0$ .  $n$ , the ideality factor can be defined by,

$$n = \frac{q}{kT} \frac{dV}{d(\ln J)} \quad (4)$$

The ideality factor was calculated to be 1.35. If  $n$  is between 1 and 2, the tunnelling current occurs in the depletion region. Barrier height can be written by equation (5)

$$\phi_b = \frac{kT}{q} \ln \left( \frac{AA^*T^2}{J_o} \right) \quad (5)$$

where  $A$ , is the contact area.  $\phi_b$ , the barrier height is calculated to be 0.46 eV. The ideal barrier height of ZnO/Si diode (the difference between the work functions of Si (4.97 eV) and ZnO (4.25 eV)) is about 0.72 eV [4]. But,  $\phi_b$  barrier height in 0.46 eV that is lower than this value.

It can be emphasized that the n-ZnO/p-Si heterojunction structure is a non-ideal diode if the height of the barrier is low and the ideality factor is greater than 1. This may have been caused by a number of circumstances: the thickness of the  $\text{SiO}_x$  layer formed at the interface during the deposition or annealing processes, the tunnelling mechanism, the formed defects depending on oxygen vacancies in the crystal structure of ZnO thin film, the morphological disorders in the ZnO thin film and the low shunt resistance in the thin film, the lattice mismatch between ZnO thin film and Si wafer [7, 26].

### 3.3. The photoelectric property of Ag/n-ZnO/p-Si/Ag heterojunction

The forward current under the  $70 \text{ mW/cm}^2$  power density illumination conditions is higher than the current in the dark (Figure 5). Short circuit current density ( $J_{sc}$ ), open

circuit voltage ( $V_{oc}$ ) and fill factor (FF) are given to be  $3 \times 10^{-2}$  mA/cm<sup>2</sup>, 100 mV and 0.20, respectively. In the forward bias region, the current in the dark exponentially increases in a very weak ratio while the current in the reverse bias region is almost negligible. However, in the illumination conditions, it has been measured that the Ag/n-ZnO/p-Si/Ag heterojunction structure exhibits the solar cell characteristics. According to J-V plot, forward current increases as the bias voltage under illumination increases. Furthermore, since the energy of the incident photon is greater than the band gap of ZnO and Si, the increase in the reverse bias current is observed [27]. If the charge carriers in the p-type Si was excited by the incident light, the photo-excited electrons were accumulated in the depletion area [28] and the photocurrent occur. However, n-ZnO/p-Si structure has very little power conversion efficiency. There may be a few cases of low efficiency: the above-mentioned situations which cause a low barrier height and a high ideal factor of 1, thick p-type Si active layer, the tunnelling mechanism, a polycrystalline structure of ZnO thin film. Because the active layer is very much thick (500  $\mu$ m), the lifetime of the photo-excited charge carriers is too low to reach the sides of the active layer and unfavourable recombination may occurs. Thus, photocurrent remains low and therefore the conversion efficiency remains low too. The undesirable charge transitions in the depletion region may occur depending on the SiO<sub>x</sub> layer or the morphological disturbances (pinholes, grain boundaries, low shunt resistivity) of ZnO thin film at the interface. Thus, recombination, occurs in the depletion region, is the source of the lower conversion efficiencies. The ZnO thin film has a polycrystalline structure and an indication of grain growth in different directions. This situation can adversely affect the density and mobility of the charge carriers on the film and hence the power conversion efficiency.

#### **4. Conclusions**

In this study, we produced Ag/ZnO/Si/Ag heterojunction structure. ZnO thin film produced in this work is transparent over a spectral range of 500-700 nm in the visible region, has relatively high absorbance in the IR region and has  $\sim 3.3$  eV band gap value. ZnO thin film is ideal for TCO material with these optical properties. Furthermore, it has a polycrystal structure and (002) main peak is dominant. J-V plot of ZnO/Si heterojunction diode was obtained under the illumination condition ( $70$  mW/cm<sup>2</sup>) and in the dark. It does not show good rectifying behaviour and diode characteristics in the dark,



but, exhibits rectifying behaviour under the illumination. Under the illumination, barrier height and ideality factor are given to be 0.46 eV and 1.35, respectively. In addition, Ag/ZnO/Si/Ag structure exhibits the solar cell behaviour with open circuit voltage of 100 mV, short circuit current density of  $3 \times 10^{-2}$  mA/cm<sup>2</sup>, fill factor of 0,20 and the relatively low power conversion efficiency is due to the photo current caused by photo-excited charge carriers in the Si active layer. The interface situations in ZnO/Si heterojunction, tunnelling mechanism, thick Si active layer, the polycrystal structure of ZnO thin film, pinholes and grain boundaries in the ZnO film which can cause the lower efficiency.

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